

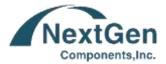
## **SPECIFICATION SHEET**

SPECIFICATION SHEET NO.	R0412- CR0805JB1KS001					
DATE	Apr. 12, 2024	Apr. 12, 2024				
REVISION	A1 Updated With Most Recent Data					
DESCRIPTION AND MAIN PARAMETRICS	Thick Film Chip Resistors, 0805 (2012 Metric), CR05 Series, Dimension L2.00*W1.25*H0.50mm, 2 Terminations Tolerance: ±5%, Resistance 1K ohm, Dissipation Max. 1/8W @ 70°C Temperature Coefficient Rate (TCR) Max. ±100ppm/°C Operating Temp. Range -55°C ~+155°C Package in Tape/Reel, 5,000pcs/Reel					
CUSTOMER	RoHS/RoHS III compliant and HF					
CUSTOMER PART NO.						
CROSS REF. PART NO.						
ORIGINAL MFG/PART NO.	Aillen/CR05JB1K					
PART CODE	CR0805JB1KSC	001				

### **VENDOR APPROVE** Issued/Checked/Approved omp mpo mpo Ruby Zhang Jack Zhang Mandy Xu

DATE: Apr. 12, 2024

**CUSTOMER APPROVE** DATE: 1 4/12/2024



# PART CODE: CR0805JB1KS001 THICK FILM CHIP RESISTORS CR05 SERIES

### DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer. The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

### **MAIN FEATURE**

- High Reliability And Stability
- Reduced Size Of Final Equipment
- Lower Assembly Costs
- Higher Component And Equipment Reliability
- RoHS III Compliant And Halogen Free Products

### APPLICATION

- Consumer Electrical Equipment
- EDP, Computer Application
- Telecom Application

### **HOW TO ORDER**

• Please indicate pat code OR custom parameters code and send us your RFQ by E-mail



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## THICK FILM CHIP RESISTORS CR05 SERIES

### PART CODE GUIDE



CODE	NAME	
CR	Product code	KEY SPECIFICATION OPTION   Thick Film Chip Resistors
0805	Size Code	0402 (1005): L1.00*W0.50mm; 0603 (1608): L1.60*W0.80mm; 0805 (2012): L2.00*W1.25mm; 1206 (3216:) L3.20*W1.60mm;
J	Resistance Range Tolerance Code	P: Jumper; B: +/-0.1%; D: +/-0.5%; F: +/-1%; J: +/-5%
В	Package Code	A: 4Kpcs/7"Reel; B:5kpcs/7"Reel; C:10kpcs/7"Reel; M:15kpcs/7"Reel; D:10kpcs/10"Reel; E:20kpcs/10"Reel
1K	Resistance value code	OR: 0ohm; 1R2: 1.2ohm; 15R: 15ohm; 20R: 20ohm; 51R: 51ohm; 100R: 100ohm; 120R: 120ohm; 1K:1Kohm; 10K: 10Kohm; 10K5: 10.5Kohm; 13K7: 13.7Kohm; 62K: 62Kohm; 100K: 100Kohm; 475K: 475Kohm; 1M: 1.0Mohm; 1M2: 1.2Mohm
S001	Internal control code,	Custom letter A~Z, a-z or digits (0-9)

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## **THICK FILM CHIP RESISTORS CR05 SERIES**

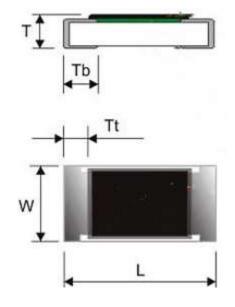
### **DIMENSION (Unit: mm)**

Image for reference



General Marking: Blank

CR05 series



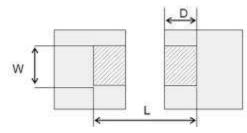
Item	Dimension		
L	2.00±0.10		
w	1.25±0.10		
т	0.50±0.15		
Тb	0.40±0.20		
Τt	0.40±0.20		



Recommended Solder Pad

**Resistors Construction** 

For Reference



ltem	Dimension	
W	1.00~1.40	
L	3.20~3.80	
D	0.90~1.40	

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**THICK FILM CHIP RESISTORS CR05 SERIES** 

### **GENERAL ELECTRONICAL CHARACTERISTICS**

IT	ITEM		SYMBOL	CHARACTERISTIC	CONDITION
Produ	Product Name		CR	Thick Film Chip Resistors	
Size			0805	CR05 Series, L2.00*W1.25*H0.50mm	
Resistar	nce Range	Ω		1K	
Resistance	Resistance Tolerance		J	±5%	
TCR	10MΩ≥ R>10Ω	ppm/°C		≤ ±100	
	R≤10Ω			-200~+400	
Max. Di	ssipation	w		1/8	@ Tamb=70°C
Operating <sup>-</sup>	Operating Temperature			-55 ~+155	
Max. Operation Voltage		V		150	@DC or RMS
Max. Overl	oad Voltage	V		300	@DC or RMS

#### Note

1) This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"

2) Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

RCWV =  $\sqrt{Rated Power \times Resistance Value}$  or Max. RCWV listed above, whichever is lower.

3) Test condition for jumper (0  $\Omega)$ 

ITEM	UNIT	SYMBOL	CHARACTERISTIC	CONDITION
Power Rating At 70°C	W		1/8	
Max. Resistance	mΩ		50	
Rated Current	A		2.0	
Peak Current	A		3.5	
Operating Temperature	°C		-55 ~+155	

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## THICK FILM CHIP RESISTORS CR05 SERIES

### PRODUCT CHARACTERIZATION

Standard values of nominal resistance are taken from the E24 & E96 series for resistors with a tolerance Of +/-0.1%, +/-0.5%, +/-1% & +/-5%, The values of the E24/E96 series are in accordance with "IEC publication 60063"

### DERATING

The power that the resistor can dissipate depends on the operating temperature; see Fig.1

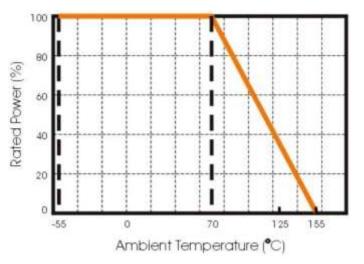


Fig 1 Maximum dissipation in percentage of rated power as a function of the ambient temperature for CR06, CR03, CR03, CR02

### MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems. Chip placement can be on ceramic substrates and printed-circuit boards (PCBs). Electrical connection to the circuit is by individual soldering condition. The end terminations guarantee a reliable contact.

### STORAGE AND HANDLING CONDITIONS

1) Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.

2) To store products with following condition: Temperature : 5 to 40°C Humidity: 20 to 70% relative humidity

3) Don't store products in a corrosive environment such as sulfide, chloride gas, or acid. It may cause oxidization of electrode, which easily be resulted in poor soldering b.To store products on the shelf and avoid exposure to moisture. Don't expose products to excessive shock, vibration, direct sunlight and so on.

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### **THICK FILM CHIP RESISTORS CR05 SERIES**

### **REFLOW SOLDERING CONDITION**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260  $^{\circ}$ C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs). Surface Mount Resistors are tested for solderability at 235  $^{\circ}$ C during 2 seconds. The test condition for no leaching is 260  $^{\circ}$ C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 2.

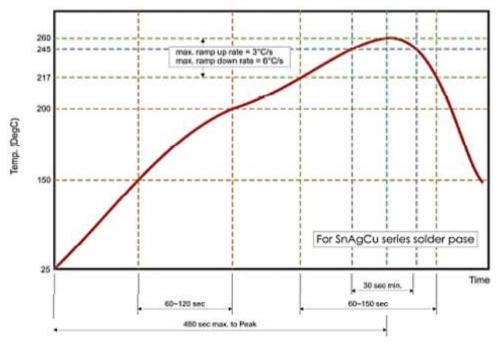


Fig 2. Infrared soldering profile for Chip Resistors

### **TEST AND REQUIREMENTS**

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS. The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied Temperature: 15 °C to 35 °C. Relative humidity: 45% to 75%. Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

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## THICK FILM CHIP RESISTORS CR05 SERIES

### TEST AND REQUIREMENT (JIS C 5201-1: 1998)

TEST	PROCEDURE / TEST METHOD	REQUIREME	ΝТ
		Resistor	0Ω
Electrical	- DC resistance values measurement	Within the	<50mΩ
Characteristics	- Temperature Coefficient of Resistance (T.C.R)	specified tolerance	
JISC5201-1: 1998	Natural resistance change per change in degree	Refer to "QUICK	
Clause 4.8	centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}$	REFERENCE DATA"	
	t1 : 20°C+5°C-1°C; t2 : -55°C or +155°C		
	R1 : Resistance at reference temperature		
	(20°C+5°C/- 1°C)		
	R2: Resistance at test temperature (-55°C or +155°C)		
Resistance To	Un-mounted chips completely immersed for 10±1	±5%: ΔR/R max.	<50mΩ
Soldering Heat(R.S.H)	second in a SAC solder bath at 260°C±5°C	±(1%+0.05Ω);	
JISC5201-1:1998		±1%: ΔR/R max.	
Clause 4.18		±(0.5%+0.05Ω)	
		no visible damage	
Solderability	Un-mounted chips completely immersed for 2±0.5	95% coverage min., g	good
JISC5201-1: 1998	second in a SAC solder bath at 235°C±5°C	tinning and no visible	damage
Clause 4.17			
Temperature cycling	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-	±5%: ΔR/R max.	<50mΩ
JISC5201-1: 1998	1°C, 30 minutes at +125°C±3°C, 2~3 minutes at	±(1%+0.05Ω);	
Clause 4.19	20°C+5°C1°C, total 5 continuous cycles	±1%: ΔR/R max.	
		±(0.5%+0.05Ω)	
		no visible damage	
High Temperature	1000+48/-0 hours; without load in a temperature	±5%: ΔR/R max.	<50mΩ
Exposure	chamber controlled 155 3 C	±(2%+0.1Ω);	
MIL-STD-202		±1%: ΔR/R max.	
method 108		±(1%+0.1Ω)	
		no visible damage	

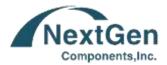
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## THICK FILM CHIP RESISTORS CR05 SERIES

### TEST AND REQUIREMENT (JIS C 5201-1: 1998)

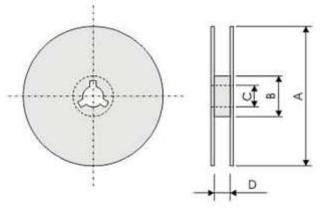
TEST	PROCEDURE / TEST METHOD	REQUIREMEI	νт
		Resistor	0Ω
Bending strength JISC5201-1: 1998 Clause 4.33	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 3mm for 10sec, 5mm for CR02	±5%: ΔR/R max. ±(1%+0.05Ω); ±1%: ΔR/R max. ±(1%+0.05Ω) no visible damage	<50mΩ
Adhesion JISC5201-1: 1998 Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec	No remarkable dama	-
Short Time Overload (STOL) JISC5201-1: 1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	±5%: ΔR/R max. ±(2%+0.05Ω); ±1%: ΔR/R max. ±(1%+0.05Ω) no visible damage	<50mΩ
Load life in Humidity JISC5201-1: 1998 Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40 C 2 C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	±5%: ΔR/R max. ±(2%+0.1Ω); ±1%: ΔR/R max. ±(1%+0.1Ω) no visible damage	<50mΩ
Load life (endurance) JISC5201-1: 1998 Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2ºC, 1.5 hours on and 0.5 Hours off	±5%: ΔR/R max. ±(3%+0.1Ω); ±1%: ΔR/R max. ±(1%+0.1Ω) no visible damage	<50mΩ
Insulation Resistance JISC5201-1: 1998 Clause 4.6	Apply the maximum overload voltage (DC) for 1minute	R≥10GΩ	
Dielectric Withstand Voltage JISC5201-1: 1998	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flas	hover
Clause 4.7 4/12/2024			9



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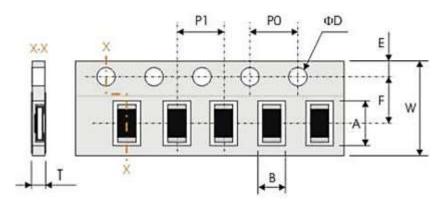
### **REEL DIMENSION (Unit: mm)**

7": 5,000Ppcs/Reel



Code	Dimension 7"	Dimension 10"	Dimension 13"	
А	178.0+/-2.0	254.0+/-2.0	330.0+/-2.0	
В	60.0 +/-1.0	100 +/-1.0	100+/-1.0	
С	13.0+/-0.20	13.0+/-0.20	13.0+/-0.20	
Т	9.0+/-0.50	9.0+/-0.5	9.0+/-0.5	

### TAPE DIMENSION (Unit: mm)



Code	Dimension
А	2.40±0.20
В	1.65±0.20
W	8.00±0.30
F	3.50±0.20
E	1.75±0.10
P 1	4.00±0. 10
PO	4.00±0.10
ΦD	1.50+0.10/- 0.0
Т	1.0 Max.

### TAPING QUANTITY AND TAPE MATERIAL

Таре	Paper Tape						Embossed Tape	Bulk Cassette
		4 mm Pitch		2 mm Pitch			4 mm Pitch	
Reel Size	7″	10"	13"	7"	10"	13″	7"	
CR05	5000	10000	20000	-	-	-	-	10000



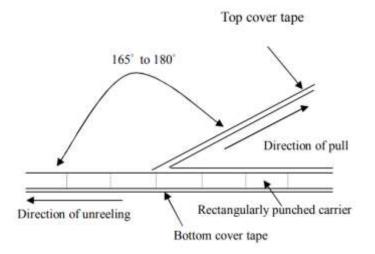
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### PERFORMANCE OF TAPING

### Strength of Carrier Tape and Top Cover Tape

Carrier Tape: When a tensile force 1.02kgf is applied in the direction of unreeling the tape, the tape shall withstand this force. Top cover Tape: When a tensile force 1.02kgf is applied to the tape, the tape shall withstand this force. Peel Force of Top Cover Tape

Unless otherwise specified, the peel force of top cover tape shall be 10.2 to 71.4 g f when the top cover tape is pulled at a speed of 300mm/min with the angle between the taped during peel and the direction of unreeling maintained at 165 to 180°as illustrated in Fig.





## THICK FILM CHIP RESISTORS CR05 SERIES

### **ROHS COMPLIANCE**

 The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU RoHS Directive (EU) 2015/863 EC (RoHS3). RoHS Test Report for this product can be obtained can be obtained at Download Center.

### **REACH COMPLIANCE**

 REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, REACH Test Report for this product can be obtained can be obtained at Download Center.

### IMPORTANT NOTES AND DISCLAIMER

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