

SPECIFICATION SHEET

SPECIFICATION SHEET NO.	R0906-XS11M05920L006
DATE	Sep. 6, 2024
REVISION	A0
DESCRIPITION	DIP Crystal, 49S Type, L11.5*W4.65*H3.5mm, 2 pins,
	11.0592MHz, +/-50ppm, CL 20pF, Stability +/-50ppm @Operating Temp.
	Range -10°C ~+60°C, ESR 50 ohm Max,
	RoHS/RoHS III compliant
CUSTOMER	
CUSTOMER PART NUMBER	
CROSS REF. PART NUMBER	
ORIGINAL PART NUMBER	TGS CS 11M0592A50-20-50-10-50 BLF
PART CODE	XS11M05920L006

VENDOR APPROVE

Issued/Checked/Approved







DATE: Sep. 6, 2024

CUSTOMER APPROVE

DATE:

9/24/2024



DIP CRYSTAL 49S TYPE 2 PINS

MAIN FEATURE

- DIP Crystal, 49S Type, L11.5*W4.65*H3.5mm, 2 pins
- Low cost, High precision, High frequency stability
- Reflow Profile Condition 260 °C Max.
- Wide Frequency Range
- Cross more competitors part
- RoHS/RoHS III compliant

APPLICATION

- Bluetooth, wireless communication set
- Communication Electronics

PART CODE GUIDE



XS	11M05920	L	006
1	2	3	4

- 1) XS: Part family Code for DIP Crystal, 49S Type, L11.5*W4.65*H3.5mm, 2 pins (CS)
- 2) 11M05920: Frequency range code for 11.0592MHz
- 3) L: DIP type, Package Bulk
- 4) 006: Specification code for original part No.: TGS CS 11M0592A50-20-50-10-50 BLF



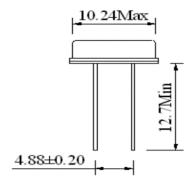
DIP CRYSTAL 49S TYPE 2 PINS

DIMENSION (Unit: mm, Tol. +/-0.15mm)

Image for reference



CS



Marking

Line 1: TGS+ CL

Line 2: Freq. Range+ Internal Code



11.5Max

H: 3.5mm



DIP CRYSTAL 49S TYPE 2 PINS

ELECTRICAL PARAMETERS

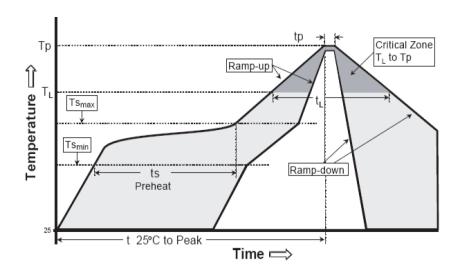
Parameter		Part No.	Units	Value		Condition	
		Symbol		Min.	Typical	Max.	
Original	Manufacturer	TGS	TGS Crystals				
Holder T	·уре	CS	DIP Crystal,	49S Type, L1	L1.5*W4.65*H3.5	mm, 2 pins	
Frequen	cy Range	11M05920	MHz	MHz 11.0592			
Mode of	Oscillation	А			AT Fundamenta	al	
Frequen	cy Tolerance	50	ppm	-50		+50	@25°C
Load Cap	pacitance	-20	pF	20			
Stability over Operation Temperance		-50	ppm	-50		+50	
Operation	on Temperance	-10	°C	-10		+60	
Storage Temperance			°C	-40		+85	
Equivalent Series Resistance (ESR)		-50	Ω			50	
Drive Level			μW			100	
Shunt Ca	Shunt Capacitance (CO)		pF	0		7.0	
Motiona (C1)	Il Capacitance		fF	N/A			
DLD2			Ω	N/A			
FLD2			ppm	N/A			
RDL2	RDL2		Ω	N/A			
SPDB			dB	N/A			
Aging			ppm/year			±5	@1 st year
Insulation Resistance			МΩ	500			@100VDC ± 15VDC
	Package	В	Bulk				
	RoHS Status	LF	RoHS III compliant				
Others	Add Value		N/A				
	Internal Control Code *		N/A				

Note: 1) Original Part Number: TGS CS 11M0592A50-20-50-10-50 BLF

DIP CRYSTAL 49S TYPE 2 PINS

SUGGESTED REFLOW PROFILE (For Reference Only)

Total time: 200 Sec. Max. Solder melting point: 220°C



Profile Feature		Pb-Free Assembly
Average Ramp-up Rate (Ts Max to Tp)		3°C/second Max
Preheat	Temperature Min (Ts Min.)	140℃
	Temperature Max (Ts Max.)	180°C
	Time (ts Min. to ts Max.)	70 ~ 100 seconds
Time maintained above	Temperature (TL)	240°C
	Time (tL)	20 ~ 50 seconds
Peak/Classification Temperature (Tp)		250 ℃
Time within 5°C of actual Peak Temperature (tp)		7 ~ 8 seconds
Ramp-down rate		6 °C /Second Max.
Time 25 °C to Peak Temperature		6 minutes Max.



DIP CRYSTAL 49S TYPE 2 PINS

CHARACTERISTICS

Units and values indicated with { } in this specification are the former units and the specified values.

Standard atmospheric conditions:

Unless otherwise specified the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15°C to 35°C Relative humidity: 25% to 85% Air pressure: 86 to 106 k Pa

If there is any doubt about the results measurements shall be made within the following limits:

Ambient temperature : $25\pm 1^{\circ}$ C Relative humidity : 63% to 67% Air pressure : 86 to 106 k Pa

Operating temperature range:

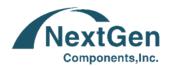
The operating temperature range is the range of ambient temperatures at which the quartz crystal oscillator can be stored without damage. Conditions are as specified elsewhere on these specifications.

Operating temperature range: -20°C to +70°C

Storage temperature range:

The storage temperature range is the range of ambient temperatures at which the quartz crystal oscillator can be stored without damage. Conditions are as specified elsewhere on these specifications.

Storage temperature range: -40°C to +85°C



DIP CRYSTAL 49S TYPE 2 PINS

Mechanical characteristics

Provided that measurement shall be carried out after letting it alone in the room temperature for 1h

Item		specifications		
1	Shock	Shock Dropping three times from the height of 50cm onto hard wooden board of thickness more than 30mm.		
		(1) Vibration Frequency	10~55HZ	
		(2) Cycle	1 to 2 Min	
2	Vibration	(3) Amplitude	0.8mm	
		(4) Direction	x. y. z	
		(5) Time	2hr for each direction	
		(1) Pulling	a) Body of specimen shall be fixed and 8.82N of tension weight shall be supplied gradually to axial direction of terminals/lead-wires for 30s	
			b) After above test a)there is no observation of any visual damages on the specimen	
3 Terminal Strength	(2) Bending	a) Body of specimen shall be fixed and 90 degree bending shall be given being supplied 225g tension weight, Afterthatterminalsl1ead-wires shall be straightened the same bending and straightening shall be supplied to the opposite direction in the same axial		
			b) After above tesla)there is no observation of any visual damages on the specimen	
4	Stealing Tightness	There is no observation of gas bubble after specimen put into alcohol below1atm for 3 min.		
5	Solder ability	Terminals/lead-wires of specimen shall be dipped into solder melted tank at230± 5°C for 3± 0.5sec. Dipping depth shall be 2mm from the bottom of specimen's body. (After applying ROSIN flux) Soldering portion shall be covered in over 90% of terminals/lead-wires dipped		
6	Resistance to Soldering Heat	Terminals/lead-wires of specimen shall be dipped into solder melted tank at $350\pm10^{\circ}\text{C}'\text{C}$ for $3\text{-}4\text{sec}$. or $250\pm5^{\circ}\text{C}$ for $5\pm1\text{sec}^*\text{Frequency}$ variation shall be within $\pm5\text{ppm}$ and equivalent resistance less than $\pm15\%$ max after the testNote: Measuring the frequency should be done after keeping test samples at room temperature for 24 hours		

^{*&#}x27;Frequency variation shall be within ± 5ppm and equivalent resistance less than ± 15% max after the test Note: Measuring the frequency should be done after keeping test samples at room temperature for 24 hours

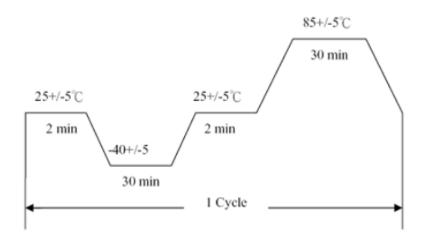


DIP CRYSTAL 49S TYPE 2 PINS

Mechanical characteristics

Provided that measurement shall be carried out after letting it alone in the room temperature for 1h

Item		specifications
1	Humidity	It alone at 40°C +-2°C in humidity of 90~95% for 48h
2	Storage in Low Temperature	It alone at -40°C +-2°C for 240h
3	Storage in High Temperature	It alone at 85°C +-2°C for 240h
4	Temperature Cycle	The following temperature cycle (10 cycles) Refer to below Fig. Temperature shift from low to high, high to low shall be done in 1°C'C /min



^{*&#}x27;Frequency variation shall be within \pm 5ppm and equivalent resistance less than \pm 15% max after the test Note: Measuring the frequency should be done after keeping test samples at room temperature for 24hours



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Notes

- 1. Only the lead should be heated when soldering In case that the package temperature is exceeding 150° C It may impair the crystal or may cause the crystal quartz 10 destroy.
- 2. Pulling the lead strongly may cause cracking of the hermetic grass seal bending the lead closely from the case may also cause same problem so when the lead needs to be bent please leave move than 05.mm of lead from the case.
- 3. Too much shock or vibration is not allowed. According to conditions such as machine shock during the assembly the internal quartz crystal might be damaged.

Please check your conditions carefully when using it in advance

- 4. Don't storage or use in the environment that temperature may change rapidly to avoid the condensation. And also we recommend to storage the products in the normal environment (Temperature humidity)
- 5. This product can be subjected to ultrasonic cleaning. However since the oscillator may be affected depending on the condition be sure to check it.
- 6. Applying excessive drive level to the quartz crystal may cause deterioration for characteristics or damage Circuit design must be such as to maintain a proper drive level.
- 7. Unless adequate negative resistance is allocated in the oscillation circuit startup time of oscillation may be increased or no oscillation may occur in order to avoid this provide enough negative resistance in the circuitry design.

DISCLAIMER

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